



# SESSION ONE

## IBM POWER6™

CHAIR DOUG BURGER, UT-AUSTIN  
9:15 - 10:45 AM

Fault-Tolerant Design of the IBM POWER6™  
Microprocessor

**Kevin Reick,  
Pia N. Sanda,  
Scott Swaney,  
Jeffrey W. Kellington,  
Michael Floyd,  
Daniel Henderson**  
IBM

System Performance Scaling of IBM POWER6™  
Based Servers

**Jeff Stuecheli**  
IBM

The 3<sup>rd</sup> Generation of IBM's Elastic Interface on  
POWER6™

**Daniel Dreps**  
IBM

